



**SPECIFICATIONS  
FOR LCD MODULE**

MATRIX ORBITAL NO.	MOP-GL24064A
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DOCUMENT REVISION HISTORY:

DATE	PAGE	DESCRIPTION
2000.8.	-	First release
2005.3.	-	Modify the full specification
2005.12	4	Update the part number system

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## **1. Precautions in use of LCD Modules**

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.

## **2. General Specification**

<b>Item</b>	<b>Dimension</b>	<b>Unit</b>
Number of Dots	240 x 64	□
Module dimension(None Backlight )	180.0 x 65.0 x 11.0□MAX□	mm
Module dimension(With Backlight )	180.0 x 65.0 x 16.0□MAX□	mm
View area	132.2 x 39.2	mm
Active area	127.16 x 33.88	mm
Dot size	0.49 x 0.49	mm
Dot pitch	0.53x 0.53	mm
LCD type	STN	
Duty	1/64	
View direction	6 o'clock or 12 o'clock	
Backlight Type	None, YELLOW-GREEN backlight,  WHITE backlight	

### **3. Absolute Maximum Ratings**

Item		Symbol	Min	Max	Unit
Input Voltage		$V_I$	-0.3	VDD+0.3	V
Supply Voltage For Logic		VDD-V <sub>SS</sub>	-0.3	7.0	V
Supply Voltage For LCD		V <sub>DD</sub> -V <sub>0</sub>	0	16	V
Standard Temperature LCM	Operating Temp.	Top	0	50	□
	Storage Temp.	Tstr	-10	60	□
Wide Temperature LCM	Operating Temp.	Top	-20	70	□
	Storage Temp.	Tstr	-30	80	□

### **4. Electrical Characteristics**

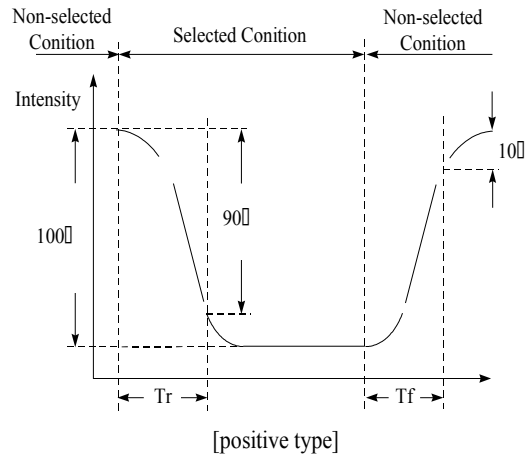
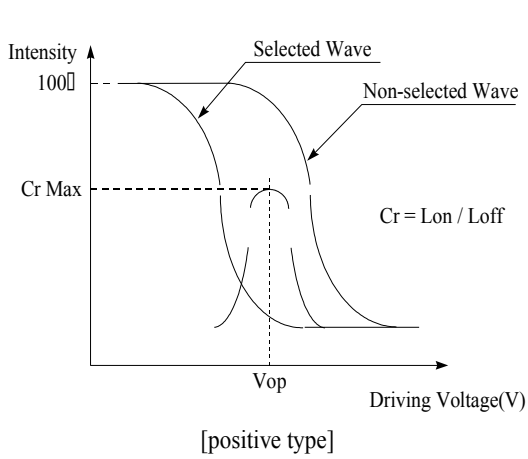
Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V <sub>DD</sub> -V <sub>SS</sub>	□	4.5	5.0	5.5	V
Supply Voltage For LCD	V <sub>DD</sub> -V <sub>0</sub>	Ta=25□	12.0	12.5	13.2	V
Input High Volt.	V <sub>IH</sub>	□	0.7 V <sub>DD</sub>	□	V <sub>DD</sub>	V
Input Low Volt.	V <sub>IL</sub>	□	V <sub>SS</sub>	□	0.3 V <sub>DD</sub>	V
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> =5V	9.0	9.9	12.0	mA
Supply Voltage of Yellow-green backlight	V <sub>LED</sub>	Forward current =630 mA  Number of LED die 2x63= 126	3.8	4.2	4.3	V
Supply Voltage of White backlight	V <sub>LED</sub>	Forward current =60 mA  Number of LED die 4	2.8	3.1	3.3	V

# 5. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	(V) $\theta$	CR $\square$ 2	-20	$\square$	35	deg
	(H) $\phi$	CR $\square$ 2	-30	$\square$	30	deg
Contrast Ratio	CR	$\square$	$\square$	3	$\square$	$\square$
Response Time	T rise	$\square$	$\square$	$\square$	250	ms
	T fall	$\square$	$\square$	$\square$	250	ms

## Definition of Operation Voltage (Vop)

## Definition of Response Time (Tr, Tf)



### Conditions :

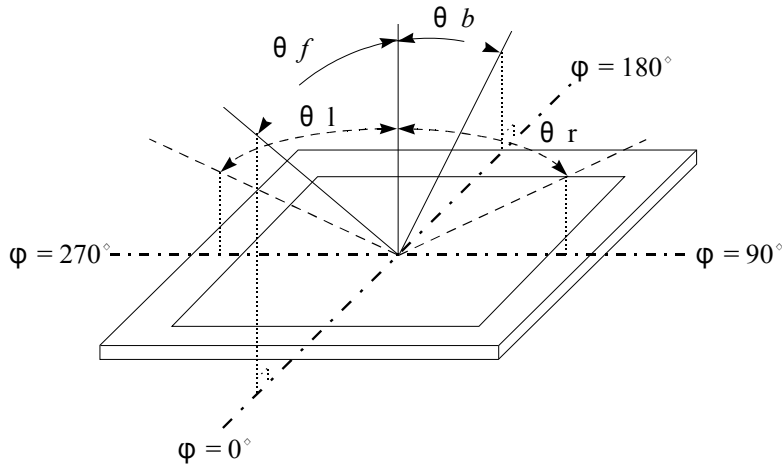
Operating Voltage : Vop

Viewing Angle( $\theta$ / $\phi$ ) : 0°  $\square$  0°

Frame Frequency : 64 HZ

Driving Waveform : 1/N duty , 1/a bias

### Definition of viewing angle(CR $\square$ 2)

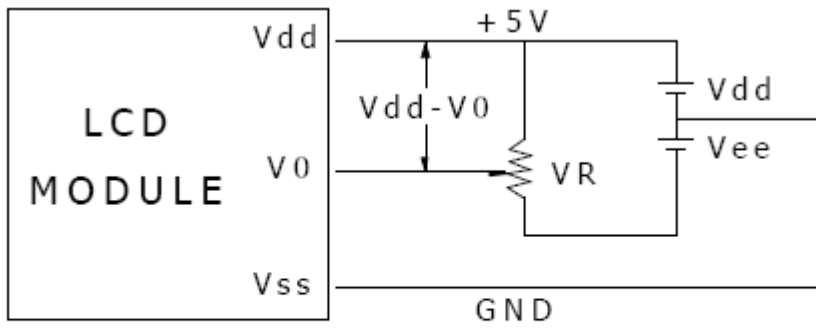


## **6. Interface Pin Function**

<b>Pin No.</b>	<b>Symbol</b>	<b>Level</b>	<b>Description</b>
1	FGND		Frame GND
2	V <sub>SS</sub>	0V	Ground
3	V <sub>DD</sub>	5.0V	Supply Voltage for logic
4	V <sub>0</sub>		Supply voltage for LCD
5	/WR	H/L	Write Data into T6963C
6	/RD	H/L	Read Data from T6963C
7	/CS	H/L	Chip enable for T6963C
8	C/D	H/L	Command/Data
9	NC		NC
10	/RST	H/L	Reset signal
11	DB0	H/L	Data bit 0
12	DB1	H/L	Data bit 1
13	DB2	H/L	Data bit 2
14	DB3	H/L	Data bit 3
15	DB4	H/L	Data bit 4
16	DB5	H/L	Data bit 5
17	DB6	H/L	Data bit 6
18	DB7	H/L	Data bit 7
19	FS	H/L	Pins for selection of font
20	NC		NC

## **7. POWER SUPPLY**

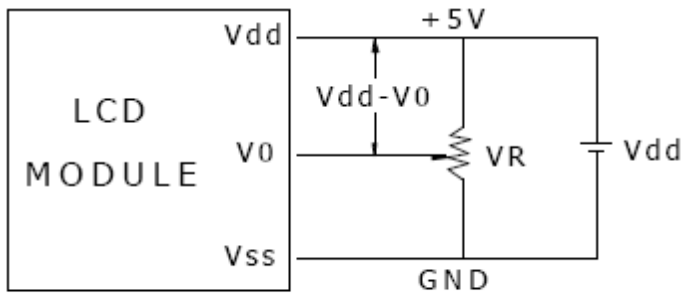
### Without Negative Power on PCB



Vdd-V0: LCD Driving Voltage

VR: 10K - 20K

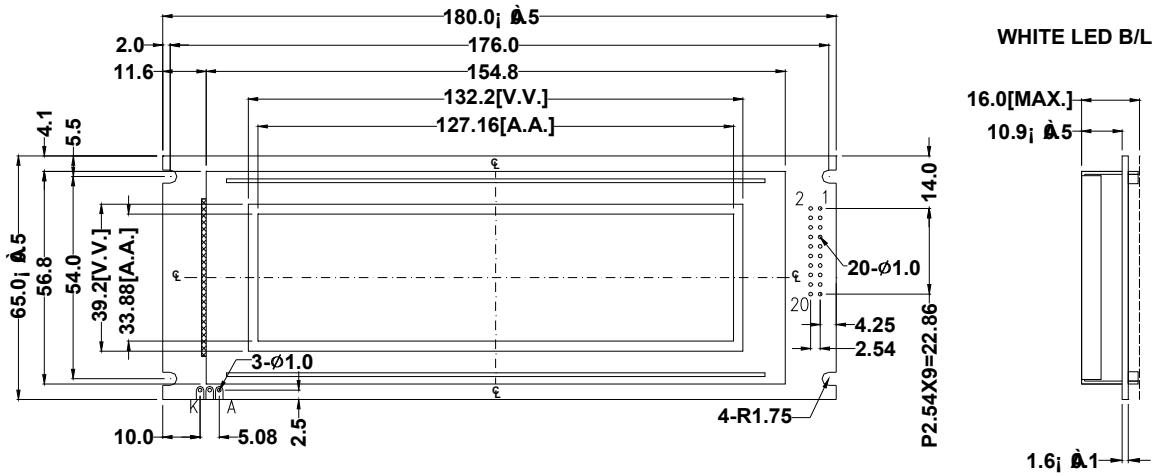
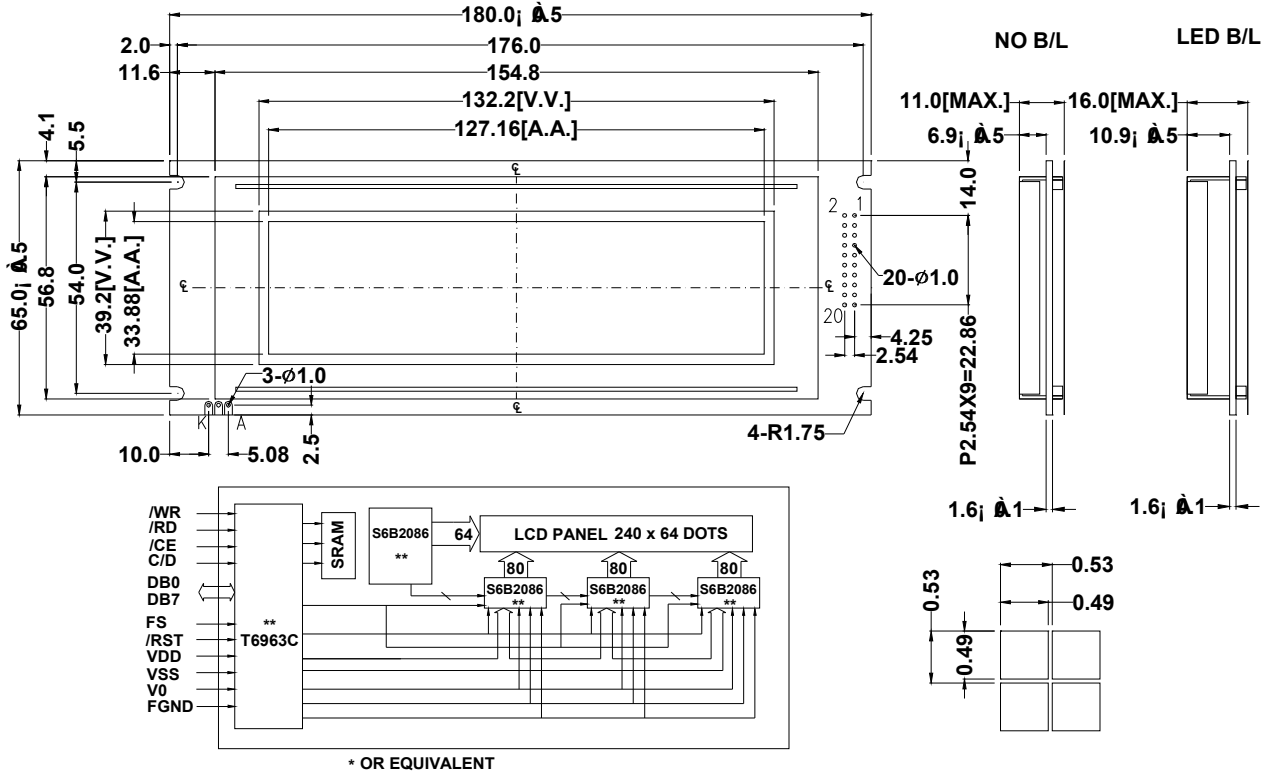
### With Negative Power on PCB



Vdd-V0: LCD Driving Voltage

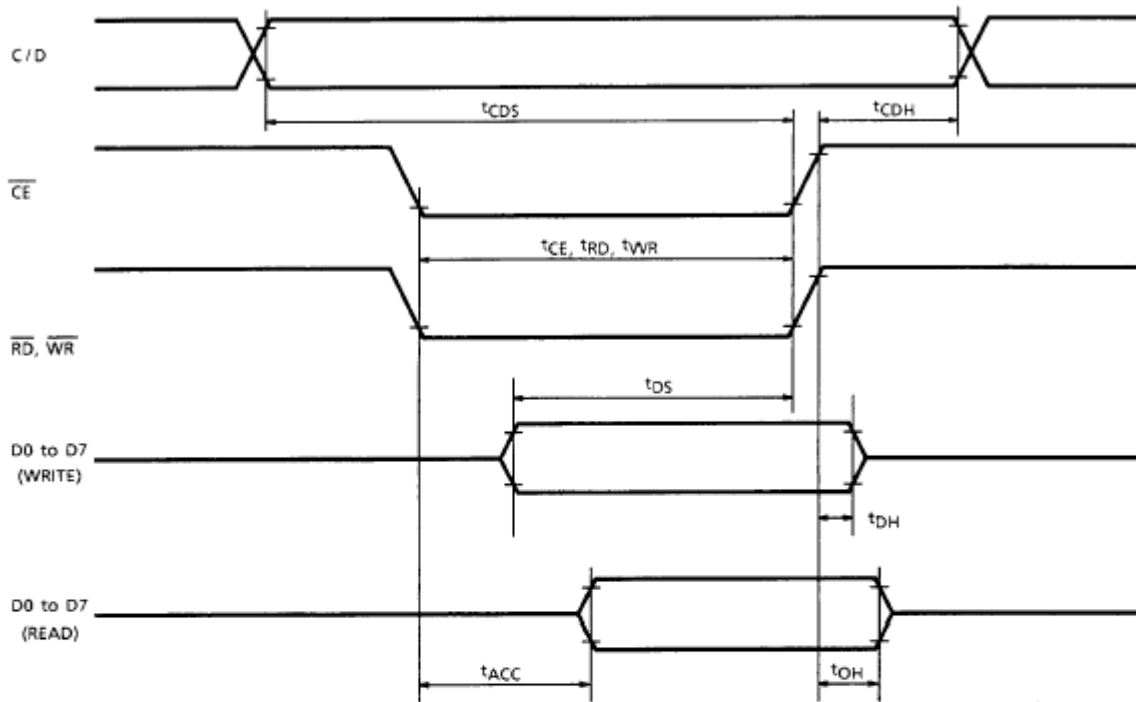
VR: 10K - 20K

# 8. Contour Drawing & Block Diagram



# 9. Timing Characteristics

## Bus Timing



TEST CONDITIONS (Unless otherwise noted,  $V_{DD} = 5.0V \pm 10\%$ ,  $V_{SS} = 0V$ ,  $T_a = -20$  to  $75^\circ C$ )

ITEM	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
C/D Set-up Time	$t_{CDS}$	—	100	—	ns
C/D Hold Time	$t_{CDH}$	—	10	—	ns
CE, RD, WR Pulse Width	$t_{CE}, t_{RD}, t_{WR}$	—	80	—	ns
Data Set-up Time	$t_{DS}$	—	80	—	ns
Data Hold Time	$t_{DH}$	—	40	—	ns
Access Time	$t_{ACC}$	—	—	150	ns
Output Hold Time	$t_{OH}$	—	10	50	ns

## 10. Table of T6963C Commands

### COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001	X address	Y address	Set Cursor Pointer
	00100010	Data	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
SET CONTROL WORD	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00H	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
MODE SET	1000X000	—	—	OR mode
	1000X001	—	—	EXOR mode
	1000X011	—	—	AND mode
	1000X100	—	—	Text Attribute mode
	10000XXX	—	—	Internal CG ROM mode
	10001XXX	—	—	External CG RAM mode
DISPLAY MODE	10010000	—	—	Display off
	1001XX10	—	—	Cursor on, blink off
	1001XX11	—	—	Cursor on, blink on
	100101XX	—	—	Text on, graphic off
	100110XX	—	—	Text off, graphic on
	100111XX	—	—	Text on, graphic on
CURSOR PATTERN SELECT	10100000	—	—	1-line cursor
	10100001	—	—	2-line cursor
	10100010	—	—	3-line cursor
	10100011	—	—	4-line cursor
	10100100	—	—	5-line cursor
	10100101	—	—	6-line cursor
	10100110	—	—	7-line cursor
	10100111	—	—	8-line cursor
DATA AUTO READ / WRITE	10110000	—	—	Set Data Auto Write
	10110001	—	—	Set Data Auto Read
	10110010	—	—	Auto Reset
DATA READ / WRITE	11000000	Data	—	Data Write and Increment ADP
	11000001	—	—	Data Read and Increment ADP
	11000010	Data	—	Data Write and Decrement ADP
	11000011	—	—	Data Read and Decrement ADP
	11000100	Data	—	Data Write and Nonvariable ADP
	11000101	—	—	Data Read and Nonvariable ADP
SCREEN PEEK	11100000	—	—	Screen Peek
SCREEN COPY	11101000	—	—	Screen Copy

X : invalid

COMMAND	CODE	D1	D2	FUNCTION
BIT SET/RESET	11110XXX	—	—	Bit Reset
	11111XXX	—	—	Bit Set
	1111X000	—	—	Bit 0 (LSB)
	1111X001	—	—	Bit 1
	1111X010	—	—	Bit 2
	1111X011	—	—	Bit 3
	1111X100	—	—	Bit 4
	1111X101	—	—	Bit 5
	1111X110	—	—	Bit 6
	1111X111	—	—	Bit 7 (MSB)

X : invalid

## **11. Quality Assurance**

### **Screen Cosmetic Criteria**

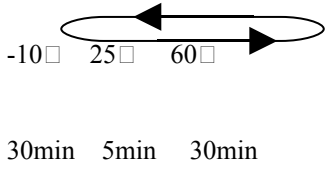
Item	Defect	Judgment Criterion	Partition
1	Spots	<p>A)Clear</p> <p><u>Size: d mm</u> <u>Acceptable Qty in active area</u></p> <p>d <math>\square</math> 0.1 Disregard</p> <p>0.1 &lt; d <math>\square</math> 0.2 6</p> <p>0.2 &lt; d <math>\square</math> 0.3 2</p> <p>0.3 &lt; d 0</p> <p>Note: Including pin holes and defective dots which must be within one pixel size.</p> <p>B)Unclear</p> <p><u>Size: d mm</u> <u>Acceptable Qty in active area</u></p> <p>d <math>\square</math> 0.2 Disregard</p> <p>0.2 &lt; d <math>\square</math> 0.5 6</p> <p>0.5 &lt; d <math>\square</math> 0.7 2</p> <p>0.7 &lt; d 0</p>	Minor
2	Bubbles in Polarizer	<p><u>Size: d mm</u> <u>Acceptable Qty in active area</u></p> <p>d <math>\square</math> 0.3 Disregard</p> <p>0.3 &lt; d <math>\square</math> 1.0 3</p> <p>1.0 &lt; d <math>\square</math> 1.5 1</p> <p>1.5 &lt; d 0</p>	Minor
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor
5	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels.	Minor

		Back-light type should be judged with back-light on state only.	
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# **12.Reliability**

## **Content of Reliability Test**

<b>Environmental Test</b>			
<b>Test Item</b>	<b>Content of Test</b>	<b>Test Condition</b>	<b>Applicable Standard</b>
High Temperature storage	Endurance test applying the high storage temperature for a long time.	60□ 96hrs	---
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-10□ 96hrs	---
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50□ 96hrs	---
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	0□ 96hrs	---
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60□,90%RH 96hrs	---
High Temperature/ Humidity Operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	50□,90%RH 96hrs	---

Temperature Cycle	Endurance test applying the low and high temperature cycle.  <p>-10°C 25°C 60°C 30min 5min 30min</p>	-10°C/60°C 10 cycles	—
Mechanical Test			
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	—
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msdc 3 times of each direction	—

\*\*\*Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25°C